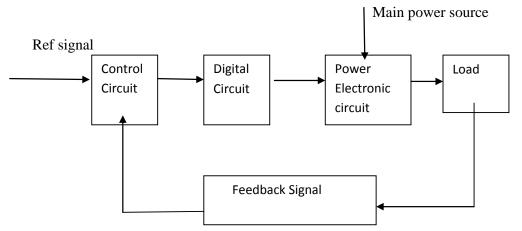
# **POWER ELECTRONICS**

The control of electric motor drives requires control of electric power. Power electronics have eased the concept of power control. Power electronics signifies the word power electronics and control or we can say the electronic that deal with power equipment for power control.



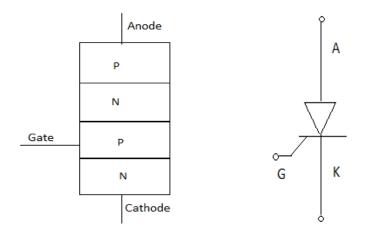
Power electronics based on the switching of power semiconductor devices. With the development of power semiconductor technology, the power handling capabilities and switching speed of power devices have been improved tremendously.

#### Power Semiconductor Devices

The first SCR was developed in late 1957. Power semiconductor devices are broadly categorized into 3 types:

- 1. Power diodes (600V,4500A)
- 2. Transistors
- 3. Thyristors (10KV,300A,30MW)

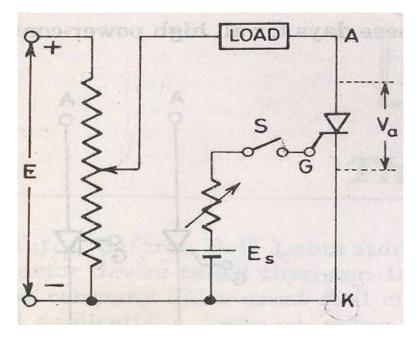
Thyristor is a four layer three junction pnpn semiconductor switching device. It has 3 terminals these are anode, cathode and gate. SCRs are solid state device, so they are compact, possess high reliability and have low loss.



SCR is made up of silicon, it act as a rectifier; it has very low resistance in the forward direction and high resistance in the reverse direction. It is a unidirectional device.

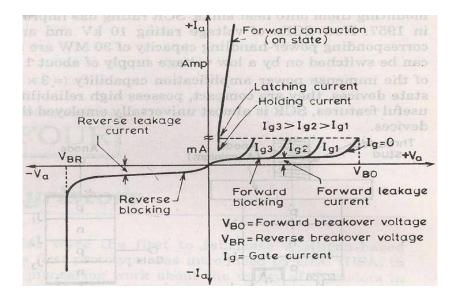
#### Static V-I characteristics of a Thyristor

The circuit diagram for obtaining static V-I characteristics is as shown



Anode and cathode are connected to main source voltage through the load. The gate and cathode are fed from source  $E_S$ .

A typical SCR V-I characteristic is as shown below:



 $V_{BO}$ =Forward breakover voltage

 $V_{BR}$ =Reverse breakover voltage

 $I_a$ =Gate current

 $V_a$ =Anode voltage across the thyristor terminal A,K.

#### $I_a$ =Anode current

It can be inferred from the static V-I characteristic of SCR. SCR have 3 modes of operation:

- 1. Reverse blocking mode
- 2. Forward blocking mode (off state)
- 3. Forward conduction mode (on state)

#### 1. <u>Reverse Blocking Mode</u>

When cathode of the thyristor is made positive with respect to anode with switch open thyristor is reverse biased. Junctions  $J_1$  and  $J_2$  are reverse biased where junction  $J_2$  is forward biased. The device behaves as if two diodes are connected in series with reverse voltage applied across them.

- A small leakage current of the order of few mA only flows. As the thyristor is reverse biased and in blocking mode. It is called as acting in reverse blocking mode of operation.
- Now if the reverse voltage is increased, at a critical breakdown level called reverse breakdown voltage  $V_{BR}$ , an avalanche occurs at  $J_1$  and  $J_3$  and the reverse

current increases rapidly. As a large current associated with  $V_{BR}$  and hence more losses to the SCR.

This results in Thyristor damage as junction temperature may exceed its maximum temperature rise.

### 2. Forward Blocking Mode

When anode is positive with respect to cathode, with gate circuit open, thyristor is said to be forward biased.

Thus junction  $J_1$  and  $J_3$  are forward biased and  $J_2$  is reverse biased. As the forward voltage is increases junction  $J_2$  will have an avalanche breakdown at a voltage called forward breakover voltage  $V_{B0}$ . When forward voltage is less then  $V_{B0}$  thyristor offers high impedance. Thus a thyristor acts as an open switch in forward blocking mode.

## 3. Forward Conduction Mode

Here thyristor conducts current from anode to cathode with a very small voltage drop across it. So a thyristor can be brought from forward blocking mode to forward conducting mode:

- 1. By exceeding the forward breakover voltage.
- 2. By applying a gate pulse between gate and cathode.

During forward conduction mode of operation thyristor is in on state and behave like a close switch. Voltage drop is of the order of 1 to 2mV. This small voltage drop is due to ohmic drop across the four layers of the device.

# **Different turn ON methods for SCR**

- 1. Forward voltage triggering
- 2. Gate triggering
- 3.  $\frac{dv}{dt}$  triggering
- 4. Light triggering
- 5. Temperature triggering

# 1. Forward voltage triggering